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## APPLICANTS

Shunpei Yamazaki, Tokyo, JAPAN;

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/094,345 06/10/1998 PAT 6,303,415 *JMM*\*\* FOREIGN APPLICATIONS \*\*\*\*\* *JMM*

JAPAN 9-168021 06/10/1997

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 10/05/2001

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	6	12	2
Verified and Acknowledged	<i>Sp. Matsu</i> Examiner's Signature <i>JMM</i> Initials				

## ADDRESS

31780  
 ERIC ROBINSON  
 PMB 955  
 21010 SOUTHBANK ST.  
 POTOMAC FALLS , VA  
 20165

## TITLE

Semiconductor device and method of fabricating same

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